

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicants: Y. Tanabe, et al.

Application No. 10/821,842

Filed: April 12, 2004

For: FABRICATION PROCESS OF A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Examiner: Ghyka, A.

Group: 2812

AMENDMENT

Mail Stop Amendment (Fee)
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed September 10, 2004, please amend the above-identified application as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.